

1 For 2891

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



APPLICATION Wolfgang Clemens et al.

SERIAL NO: 10/535,448 GROUP ART 2891
UNIT:

FILED: September 19, 2005 EXAMINER: Not assigned

: CUSTOMER NO. 27162

FOR: Electronic Component Comprising a Patterned Semi-conducting
Functional Layer and a Method for Producing Said Component

ATTY/DKT NO.: 411000-131

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DISCLOSURE STATEMENT UNDER 37 CFR 1.56

SIR:

This paper is to bring to the attention of the PTO the following U.S. copending applications, all of which are related in different respects to organic electronic devices and/or method of making such devices such as transistors, diodes, integrated circuits and the like and/or related technologies. Many of these applications and the instant above-entitled application may have one or more common inventors.

This paper is submitted in compliance with MPEP 2001.06(b) and 2004. Paper copies of these applications as well as copies of applicants' copending applications cited in prior submitted Disclosure Statements are not required under MPEP 609.04(a) II and (c), wherein the requirement to submit copies to the PTO is waived. See the cited 1287 O.G. 163 (10/19/04).

The enclosed PTO 1449 lists related applications noted below. It is respectfully requested that the Examiner consider and make of record all of the cited applications listed in the enclosed PTO 1449 and in all prior submitted similar Disclosure Statements by initialing each item and returning a copy of the initialed paper to applicants.

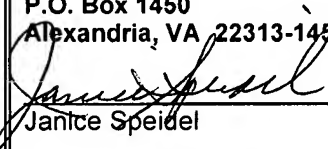
Serial No.	Title	Inventors	Our Ref.
10/562,989	Method and Device for Patterning Organic Layers	Jurgen Ficker et al.	411000-143
10/562,869	Logic Gate With A Potential-Free Gage Electrode for Organic Integrated Circuits	Wolfram Glauert et al.	411000-144
10/569,763	Organic Electronic Component With High Resolution Structuring And Method For The Production Thereof	Walter Fix et al.	411000-146
10/568,730	Organic Capacitor With Voltage-Controlled Capacitance	Wolfgang Clemens et al.	411000-147
10/569,233	Polymer Mixtures for Printed Electronic Circuits	Adolf Bernds et al.	411000-148
10/570,571	Mechanical Control Elements for Organic Polymer Electronic Devices	Wolfgang Clemens et al.	411000-149
10/585,775	Organic Transistor Comprising a Self-aligning Gate Electrode and Method for the Production Thereof	Walter Fix et al.	411000-150
11/574,139	External Package Capable of Being Radio-Tagged	Jurgen Ficker et al.	411000-153

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

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Janice Spidel

May 18, 2007
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Respectfully submitted,
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Substitute for form 449A/PTO

Complete if Known

Application Number	10/535,448
Filing Date	September 19, 2005
First Named Inventor	Wolfgang Clemens
Group Art Unit	2891
Examiner Name	Not assigned
Attorney Docket Number	411000-131

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1		1
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U.S. PATENT DOCUMENTS

Examiner Initial*	Cite No. ¹	Document Number	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kid Code ² (if known)			
					See Disclosure Statements filed herewith and previously
	143	US-10/562,989	06-29-2006	Jurgen Ficker et al.	
	144	US_10/52,869	10-05-2006	Walter Fix et al.	
	146	US-10/569,763	N/A	Walter Fix et al.	
	147	US-10/568,730	02-08-2007	Wolfgang Clemens et al.	
	148	US-10/569,233	01-25-2007	Adolf Bernds et al.	
	149	US-10/570,571	01-11-2017	Wolfgang Clemens et al.	
	150	US-10/585,775	N/A	Walter Fix	
	153	US-11/574,139	N/A	Jurgen Ficker	
Examiner Signature				Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.